

# OMLEX MX Terminal – Installation and configuration

Written By: Josef Roupec

BIOS Information		▲ Set the Date. Use Tab to
BIOS Vendor	American Megatrends	switch between Date elements
Core Version	4.6.5.4	section between bate erements.
Compliancy	UEFI 2.3.1; PI 1.2	
Project Version	E434AR38.ROM	
Build Date and Time	09/22/2015 17:08:16	
iWDD Vendor	iEi	
iWDD Version	E434ER16.bin	
IPMI Module	N/A	
Processor Information		
Name	Haswell	++: Select Screen
Brand String	Intel(R) Celeron(R) CPU	
Frequency	2800 MHz	Enter: Select
Processor ID	306c3	+/-: Change Opt.
Stepping		F1: General Help F2: Previous Values
Number of Processors	2Core(s) / 2Thread(s)	F3: Optimized Defaults
Microcode Revision	1c	F4: Save & Exit
GT Info	Not Applicable	ESC: Exit
IGFX VBIOS Version	N/A	
Memory RC Version	1.6.2.1	
Total Memory	4096 MB (DDR3)	•

#### Step 1 — Preparation

| IDS Vendor<br>pre Version     American Megatrends     switch between Date elements.       ompliancy     UEFI 2.3.1; PI 1.2     switch between Date elements.       ompliancy     UEFI 2.3.1; PI 1.2     switch between Date elements.       ompliancy     UEFI 2.3.1; PI 1.2     switch between Date elements.       uild Date and Time     09/22/2015 17:08:16     switch between Date elements.       WDD Vendor     IEI     switch between Date elements.       wDD Vendor     IEI     switch between Date elements.       wDD Version     E434ER16.bin     switch between Date elements.       rocessor Information     switch between Date elements.     switch between Date elements.       ame     Haswell     switch between Date elements.       rocessor Information     switch between Date elements.     switch between Date elements.       requency     2800 MH2     switch between Date elements.       rocessor ID     306c3     switch between Date elements.       repping     Con MH2     switch between Date elements.       umber of Processors     2Core(s) / 2Thread(s)     Fil General Help       F3: Optimized efaults     F3: Optimized efaults  | COS Vendor     American Megatrends     switch between Date elements.       mpliancy     UEFI 2.3.1; PI 1.2     switch between Date elements.       voject Version     E344R38.R0H     switch between Date elements.       vild Date and Time     09/22/2015 17:08:16     switch between Date elements.       VDD Vendor     IEi     E434ER16.bin     switch between Date elements.       VMI Module     N/A     switch between Date elements.     switch between Date elements.       *MI Module     N/A     switch between Date elements.     switch between Date elements.       *MI Module     N/A     switch between Date elements.     switch between Date elements.       *MI Module     N/A     switch between Date elements.     switch between Date elements.       *MI Module     N/A     switch between Date elements.     switch between Date elements.       *MI Module     N/A     switch between Date elements.     switch between Date elements.       *MI Module     N/A     switch between Date elements.     switch between Date elements.       *MI Module     N/A     switch between Date elements.     switch between Date elements.       *MI Module     N/A     switch between Date elements.     switch between Date elements.       *MI Module     N/A     switch between Date elements.     switch between Date elements.       ***: Select Screen  | COS Vendor<br>pre Version     American Megatrends     switch between Date elements.       ampliancy     UEFI 2.3.1; PI 1.2     switch between Date elements.       ampliancy     UEFI 2.3.1; PI 1.2     switch between Date elements.       ampliancy     UEFI 2.3.1; PI 1.2     switch between Date elements.       ampliancy     UEFI 2.3.1; PI 1.2     switch between Date elements.       ampliancy     E434R38.R0H     switch between Date elements.       AUD Vendor     IEI     switch between Date elements.       AUD Version     E434ER16.bin     switch between Date elements.       PMI Module     N/A     switch between Date elements.       ame     Haswell     switch between Date elements.       ame     Haswell     switch between Date elements.       amad String     Intel(R) Celeron(R) CPU     switch between Date elements.       amad String     Intel(R) Celeron(R) CPU     switch between Date elements.       amad String     Intel(R) Celeron(R) CPU     switch between Date elements.       amad String     Intel(R) Celeron(R) CPU     switch between Date elements.       amad String     Intel(R) Celeron(R) CPU     switch between Date elements.       amad String     CO     Switch between Date elements.       amad String     CO     Switch between Date elements.       amad String     CO     Switch  | IDS Vendor     American Megatrends     suitch between Date elements.       mpliancy     UEFI 2.3.1; PI 1.2     suitch between Date elements.       mpliancy     UEFI 2.3.1; PI 1.2     suitch between Date elements.       vojact Version     E434R38.R0H     suitch between Date elements.       NDD Vendor     E1     suitch between Date elements.       NDD Vendor     E1     suitch between Date elements.       NDD Version     E434ER16.bin     suitch between Date elements.       PMI Module     N/A     suitch between Date elements.       me     Haswell     suitch between Date elements.       and String     Intel(R) Celeron(R) CPU     suitch between Date elements.       and String     Intel(R) Celeron(R) CPU     Enter: Select Item       and String     2000 MH2     Fit: General Help       occessor ID     306c3     Fit: General Help       Jumber of Processors     2Core(s) / 2thread(s)     Fit: General Help       Fit: Save & Exlt     Fit: Save & Exlt       Esc: Exit     Esc: Exit  | OS Vendor     American Megatrends       re Version     4.6.5.4       mpllancy     UEFI 2.3.1; PI 1.2       oject Version     E4940R38.R0M       11d Date and Time     09/22/2015 17:08:16       DD Vendor     1E1       DD Version     E434ER16.bin       MI Hodule     N/A       occessor Information     Haswell       me     Haswell       me     Haswell       requency     2800 MH2       version ID     306c3       eppIng     C0       cocessors ID     200ce(s) / 2Thread(s)       crocode Revision     1c       F1 Select Defaults  | ODS Vendor     American Megatrends     switch between Date elements.       ine Version     4.6.5.4     switch between Date elements.       ingliancy     UEFI 2.3.1; PI 1.2     switch between Date elements.       ioject Version     E434R83.R0H     switch between Date elements.       ild Date and Time     09/22/2015 17:08:16     switch between Date elements.       iDD Vendor     IEI     switch between Date elements.       iDD Version     E434R83.R0H     switch between Date elements.       iDD Version     E434R16.bin     switch between Date elements.       ind Date and Time     09/22/2015 17:08:16     switch between Date elements.       iDD Version     E434R16.bin     switch between Date elements.       ind String     Intel(R) Celeron(R) CPU     th: Select Screen       ind astring     Intel(R) Celeron(R) CPU     Enter: Select Item       import of Processors ID     306c3     t/-: Change Opt.       import of Processors     2Core(S) / 2Thread(S)     F2: Previous Values       import of Processors     2Core(S) / 2Thread(S)     F3: Optimized Defaults       ind string     Ic     F4: Save & Exit  | DS Vendor American Megatrends<br>her Version 4.6.5.4<br>polancy UEFI 2.3.1; PI 1.2<br>oject Version E434AR38.ROM<br>MI Module 04/22/2015 17:08:16<br>DD Vendor 1E1<br>DD Version E434ER16.bin<br>MI Module N/A<br>add String Intel(R) Celeron(R) CPU<br>and String Intel(R) Celeron(R) CPU<br>auency 2800 MH2<br>accessor ID 306c3<br>epping C0<br>epping C0<br>epping C0<br>epping C0<br>fri Select Screen<br>fl: Select Item<br>Enter: Select 4<br>Fri Select Screen<br>fl: Select Item<br>Enter: Select 4<br>Fri Select 1<br>Fri Select 1  | S Vendor American Megatrends evicts of the UG Gravitation of the UG Gravitatio Of Gravitation of the UG Gravit   | S Vendor American Megatrends<br>e Version 4.6.5.4<br>pliancy UEFI 2.3.1; PI 1.2<br>ject Version E434ARS3.ROM<br>1d Date and Time 09/22/2015 17:08:16<br>D Vendor IE1<br>D Version E434ARS16.bin<br>II Module N/A<br>tessor Information<br>e Haswell<br>Ind String Intel(R) Celeron(R) CPU<br>Haswell **: Select Screen<br>the Haswell<br>II Select Item<br>the: Select Item<br>the: Select Item<br>T1: Select Item<br>the: Select Item<br>T1: Select Item<br>Select Item<br>T1: Select                | DS Vendor American Megatrends<br>re Version 4.6.5.4<br>ppliancy UEFI 2.3.1; PI 1.2<br>pject Version E434RA38.ROM<br>Do Vendor E434RA38.ROM<br>DO Vendor E434RA38.ROM<br>DO Version E434RA38.ROM<br>MI Module N/A<br>and String Intel(R) Celeron(R) CPU<br>and String Intel(R) Celeron(R) CPU<br>ber of Processors 2Core(S) / 2Thread(S)<br>F2: Previous Values<br>F2: Previous Values<br>F3: optimized Offaults  | COS Vendor     American Megatrends       ine Version     4.6.5.4       impliancy     UEFI 2.3.1; PI 1.2       id Oata and Time     09/22/2015 17:08:16       NOD Vendor     1E1       NOD Version     E434R83.RDH       id Oata and Time     09/22/2015 17:08:16       NOD Version     E434R83.RDH       vocessor Information     Haswell       ame     Haswell       ame     Intel(R) Celeron(R) CPU       requency     2800 MH2       requency     2800 MH2       requency     2800 MH2       repring     CO       Imber of Processors     2Corre(s) / 2Thread(s)                             | IIDS Vendor     American Megatrends     Switch between Date elements.       Jonp Liancy     UEFI 2.3.1; PI 1.2     Switch between Date elements.       Jonp Liancy     UEFI 2.3.1; PI 1.2     Switch between Date elements.       Jong Liancy     UEFI 2.3.1; PI 1.2     Switch between Date elements.       Juild Date and Time     09/22/2015 17:08:16     Switch between Date elements.       JUD Version     E434R838.R0M     Switch between Date elements.       LNDD Version     E434ER16.bin     Switch between Date elements.       Processor Information     Haswell     Switch between Date elements.       Jarand String     Intel(R) Celeron(R) CPU     H: Select Screen       Processor ID     306c3     H: Change Opt.       Stepping     C0     F1: General Hein   | IS Vendor American Megatrends<br>e Version 4,6.5.4<br>bilancy UEFI 2.3.1; PI 1.2<br>bject Version E4344R38.ROM<br>1d Date and Time 09/22/2015 17:08:16<br>iD Vendor 1E1<br>iD Version E434ER16.Din<br>fI Module N/A<br>bccssor Information<br>me Haswell<br>md String Intel(R) Celeron(R) CPU<br>squency 2800 MHz<br>spessor ID 30623<br>+*: Select Screen<br>11: Select Item<br>Enter: Select 1<br>+*: Construction 1<br>+*: Select Screen<br>11: Select Item<br>Enter: Select 1<br>+*: Construction 1<br>**: Select Screen<br>1<br>**: Select Screen<br>1<br>**: Select Screen<br>1<br>**: Select Screen<br>1<br>**: Select Screen<br>1<br>**: Select Item<br>Enter: Select 1<br>**: Construction 1<br>**: Select Screen<br>1<br>**: Select Screen<br>**: Select Screen<br>**: Select Screen<br>**: Select Screen<br>**: Select Screen<br>**: Select Item<br>Enter: Select 1<br>**: Select 1<br>**: Select 1<br>**: Select Screen<br>**: Select Screen  | IS Vendor American Megatrends<br>e Version 4.6.5.4<br>bilancy UEFI 2.3.1; PI 1.2<br>bject Version E434R83.ROM<br>Id Date and Time 09/22/2015 17:08:16<br>by Vendor 1E1<br>by Version E434ER16.bin<br>fI Module N/A<br>bcessor Information<br>me Haswell<br>and String Intel(R) Celeron(R) CPU<br>squency 2800 MHz<br>spessor ID 30623  | DS Vendor American Hegatrends<br>re Version 4.6.5.4<br>mpliancy UEFI 2.3.1; PI 1.2<br>oject Version E434RA30.ROM<br>IID Date alements.<br>switch between Date elements.<br>switch between Date element   | IS Vendor American Megatrends<br>e Version 4.6.5.4<br>bilancy UEFI 2.3.1; PI 1.2<br>bject Version E4344R38.ROM<br>100 Vendor 1E1<br>100 Version E434ER16.bin<br>4I Module N/A<br>bccessor Information<br>me Haswell<br>and String Intel(R) Celeron(R) CPU<br>gquency 2800 MHz<br>avecsor ID 30623  | OS Vendor     American Hegatrends     switch between Date elements.       re Version     4.6.5.4     switch between Date elements.       oject Version     E434R838.R0M     switch between Date elements.       11d Date and Time     09/22/2015 17:08:16     switch between Date elements.       DD Vendor     1E1       DD Version     E434ER16.bin       MI Hodule     N/A       ocessor Information     Haswell       me     Haswell       and String     Intel(R) Celeron(R) CPU       equency     2800 MHz       excessor ID     30653  | S Vendor American Megatrends<br>e Version 4,6,5,5,4<br>pliancy UEFI 2,3,1; PI 1.2<br>ject Version E434AR38.ROM<br>10 Date and Time 09/22/2015 17:08:16<br>D Vendor 1E1<br>0 Version E434ER16.bin<br>I Module N/A<br>cessor Information<br>e Haswell<br>nd String Intel(R) Celeron(R) CPU<br>guency 2800 MHz<br>version TD 306C3        | OS Vendor     American Megatrends       re Version     4.6.5.4       mpilancy     UEFI 2.3.1; PI 1.2       oject Version     E434R838.ROM       ild Date and Time     09/22/2015 17:08:16       DD Vendor     IEI       DD Version     E434ER16.bin       MI Module     N/A       occessor Information     Haswell       me     Haswell       me     Haswell       mercessor Information     Intel(R) Celeron(R) CPU       equency     2800 MHz       expressor In     30653   | S Vendor American Megatrends<br>e Version 4,6,5,5,4<br>pliancy UEFI 2,3,1; PI 1.2<br>ject Version E434AR38.ROM<br>10 Date and Time 09/22/2015 17:08:16<br>D Vendor 1E1<br>0 Version E434ER16.bin<br>I Module N/A<br>cessor Information<br>e Haswell<br>nd String Intel(R) Celeron(R) CPU<br>guency 2800 MHz<br>version TD 306C3        | DS Vendor American Megatrends<br>re Version 4.6.5.4<br>polancy UEFI 2.3.1; PI 1.2<br>oject Version E4940R38.ROM<br>IId Date and Time 09/22/2015 17:08:16<br>DD Vendor IEI<br>DD Vension E434ER16.bin<br>MI Module N/A<br>occessor Information<br>me<br>and String Intel(R) Celeron(R) CPU<br>and String Enter: Select Screen<br>II: Select Item<br>Enter: Select | DS Vendor     American Megatrends       re Version     4.6.5.4       ppliancy     UEFI 2.3.1; PI 1.2       sjject Version     E4340R38.R0H       11d Date and Time     09/22/2015 17:08:16       D0 Vendor     1E1       D0 Version     E434ER16.bin       4I Module     N/A       me     Haswell       md String     Intel(R) Celeron(R) CPU       equency     2800 MHz       cessor ID     306c3   | Solution     American Megatrends       re Version     4.6.5.4       pollancy     UEFI 2.3.1; PI 1.2       sject Version     E4940R38.R0H       11d Date and Time     09/22/2015 17:08:16       DD Vendor     1E1       DD Version     E434ER16.bin       MI Module     N/A       Decessor Information     Haswell       me     Haswell       and String     Intel(R) Celeron(R) CPU       equency     2800 MH2       ocessor ID     306c3  | Solution     American Megatrends       re Version     4.6.5.4       pollancy     UEFI 2.3.1; PI 1.2       sject Version     E4940R38.R0H       11d Date and Time     09/22/2015 17:08:16       DD Vendor     1E1       DD Version     E434ER16.bin       MI Module     N/A       Decessor Information     Haswell       me     Haswell       and String     Intel(R) Celeron(R) CPU       equency     2800 MH2       ocessor ID     306c3  |
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Taylect Version E434AR38.R0M Uild Date and Time 09/22/2015 17:08:16 NOD Vendor IEI ROD Vendor E434ER16.bin PMI Module N/A Processor Information ame Haswell The Haswell Intel(R) Celeron(R) CPU Pequency 2800 MHZ Pequency 2800 MHZ The Pequency Co tepping Co tep	Toject Version     E434R38.R0H       09/22/2015 17:08:16     09/22/2015 17:08:16       NDD Vendor     IEI       NDD Version     E434R16.bin       MI Module     N/A       Processor Information     Haswell       ame     Haswell       requency     2800 MHz       Processor ID     306c3       processor ID     2600 KHz       increpting     CO       processor S     2Core(s) / 2Thread(s)       F2: Previous Values       F3: Optimized Defaults       F4: Save & Exit       Ence:       F3: Save & Exit	Poject Version     E434R38.R0M       00/22/2015 17:08:16     09/22/2015 17:08:16       40D Vendor     IEI       40D Vendor     IEI       60D Version     E434ER16.bin       PMI Module     N/A       Processor Information     Haswell       ame     Haswell       requency     2800 MHz       Processor ID     306c3       Cocessor ID     260c MHz       Lepping     Co       Lincode Revision     1c       T Info     Not Applicable
uild Date and Time     09/22/2015 17:08:16       WDD Vendor     IEi       WDD Version     E434ER16.bin       PMI Module     N/A       Processor Information     ++: Select Screen       ame     Intel(R) Celeron(R) CPU       requency     2800 MHz       requency     2800 MHz       requency     2800 MHz       umber of Processors ID     306c3       umber of Processors     2Core(s) / 2Thread(s)       Icrocode Revision     Ic       Iz Info     Not Applicable	Hild Date and Time     09/22/2015 17:08:16       NDD Vendor     IEi       NDD Version     E434ER16.bin       NMI Module     N/A       Processor Information     Haswell       and String     Intel(R) Celeron(R) CPU       equency     2800 MH2       rocessor ID     306c3       increating     Context       umber of Processors     2Core(s) / 2Thread(s)       revelution     1c	Hild Date and Time     09/22/2015 17:08:16       HDD Vendor     LEi       HDD Version     E434ER16.bin       PMI Hodule     N/A       Processor Information     Haswell       and String     Intel(R) Celeron(R) CPU       requency     2800 MH2       rocessor ID     306c3       umber of Processors     2Core(s) / 2Thread(s)       F1: General Help       Icrocode Revision     1c       T Info     Not Applicable
  | 11d Date and Time     09/22/2015 17:08:16       DD Vendor     1E1       DD Version     E434ER16.bin       MI Module     N/A       occessor Information     Haswell       me     Intel(R) Celeron(R) CPU       equency     2800 MH2       occessor ID     306c3       reppIng     C0       provide Revision     1c       corde Revision     1c       F1 Select Versen     F1 General Help       F2 Previous Values     F3: Optimized Defaults       F3: Optimized Defaults     F3: Select Values  | Hild Date and Time     09/22/2015 17:08:16       DD Vendor     IEI       DD Version     E434ER16.bin       MI Module     N/A       Module     N/A       and String     Intel(R) Celeron(R) CPU       requency     2800 MH2       requency     2800 MH2       reping     CO       mber of Processors     2Core(S) / 2Thread(S)       F2: Frevious Values       F3: General Help       F3: General Help       F4: Save 8 Exit  | Iid Date and Time     09/22/2015 17:08:16       DD Vendor     IEi       DD Version     E434ER16.bin       MI Module     N/A       me     Haswell       and String     Intel(R) Celeron(R) CPU       equency     2800 MH2       occessor ID     306c3       epping     C0       provide Revision     1c       crocode Revision     1c       Not Applicable     F1 Select Official Celeration   | 1d Date and Time     09/22/2015 17:08:16       D Vendor     1Ei       D Version     E434ER16.bin       IM Module     N/A       Me     Haswell       ind String     Intel(R) Celeron(R) CPU       iquency     2800 MHz       cessor ID     306c3       ipping     C0       procede Revision     1c       Intel     > 21hread(s)       Fil General Help       Fil General H   | 1d Date and Time     09/22/2015 17:08:16       1d Date and Time     09/22/2015 17:08:16       10 Vendor     IE1       10 Version     E434ER16.bin       11 Module     N/A       icessor Information     +: Select Screen       reguency     2800 MHz       vicessor ID     306c3       pipins     C0       ober of Processors     2Core(s) / 2Thread(s)       F2: Previous Values       F2: Provious Values  
   | Iid Date and Time     09/22/2015 17:08:16       DD Vendor     IE1       DD Version     E434ER16.bin       WI Module     N/A       and String     Intel(R) Celeron(R) CPU       and String     Intel(R) Celeron(R) CPU       equency     2000 MHz       ocessor ID     306c3       epping     C0       rococde Revision     1c  | 111d Date and Time     09/22/2015 17:08:16       NDD Vendor     IEI       NDD Version     E434ER16.bin       WI Module     N/A       me     Haswell       and String     Intel(R) Celeron(R) CPU       requency     2800 MHZ       rocessor ID     306c3       tepping     C0       mber of Processors     2Corre(s) / 2Thread(s)   | Wild Date and Time     09/22/2015 17:08:16       UHDD Vendor     IEI       UND Version     E434ER16.bin       UPT Module     N/A       Processor Information     Haswell       Ame     Haswell       Arand String     Intel(R) Coleron(R) CPU       Processor ID     306c3       Stepping     C0  | Id Date and Time     09/22/2015 17:08:16       DD Vendor     IEI       DD Version     E434ER16.bin       MI Module     N/A       Decessor Information     Haswell       me     Haswell       Intel(R) Celeron(R) CPU     11: Select Screen       gauency     2800 MHz       resesor TD     306c3  | Id Date and Time     09/22/2015 17:08:16       DD Vendor     IEI       DD Version     E434ER16.bin       MI Module     N/A       Decessor Information     Haswell       Ind String     Intel(R) Celeron(R) CPU       audersy     2800 MHz       resesor ID     306c3   
   | Iid Date and Time     09/22/2015 17:08:16       DD Vendor     IEi       DD Version     E434ER16.bin       MI Module     N/A       accessor Information     Haswell       me     Haswell       and String     Intel(R) Celeron(R) CPU       equency     2800 MHz       cressor In     30653   | Id Date and Time     09/22/2015 17:08:16       pp Vendor     iEi       pp Vension     E434ER16.bin       A1 Module     N/A       bccessor Information     Haswell       nd String     Intel(R) Celeron(R) CPU       gauency     2800 MHz       resesor TD     306c3  | 11d Date and Time     09/22/2015 17:08:16       DD Vendor     1E1       DD Version     E434ER16.bin       MI Hodule     N/A       occessor Information     Haswell       mme     Haswell       and String     Intel(R) Celeron(R) CPU       equency     2800 MHz       errorsor TD     30653  | 10 Date and Time     09/22/2015 17:08:16       D Vendor     1E1       D Version     E434ER16.bin       I Module     N/A       cessor Information     ++: Select Screen       nd String     Intel(R) Celeron(R) CPU       quency     2800 MHz       cessor ID     306c3   | 11d Date and Time     09/22/2015 17:08:16       DD Vendor     1E1       DD Version     E434ER16.bin       WI Module     N/A       rocessor Information     Haswell       mme     Haswell       rand String     Intel(R) Celeron(R) CPU       requency     2800 MHz       requency TD     30653   
   | 10 Date and Time     09/22/2015 17:08:16       D Vendor     1E1       D Version     E434ER16.bin       I Module     N/A       cessor Information     ++: Select Screen       nd String     Intel(R) Celeron(R) CPU       quency     2800 MHz       cessor ID     306c3   | Iid Date and Time     09/22/2015 17:08:16       DD Vendor     IEi       DD Version     E434ER16.bin       MI Module     N/A       occessor Information     Haswell       me     Intel(R) Celeron(R) CPU       and String     Intel(R) Celeron(R) CPU       equency     2800 MH2       occessor ID     306c3  | Alid Date and Time     09/22/2015 17:08:16       OD Vendor     IEi       DD Version     E434ER16.bin       AI Module     N/A       Decessor Information     Haswell       me     Intel(R) Celeron(R) CPU       and String     Intel(R) Celeron(R) CPU       equency     2800 MHz       cessor ID     306c3   | lid Date and Time     09/22/2015 17:08:16       DD Vendor     IEi       DD Version     E434ER16.bin       MI Module     N/A       me     Haswell       and String     Intel(R) Celeron(R) CPU       equency     2800 MH2       ocessor ID     306c3  | lid Date and Time     09/22/2015 17:08:16       DD Vendor     IEi       DD Version     E434ER16.bin       MI Module     N/A       me     Haswell       and String     Intel(R) Celeron(R) CPU       equency     2800 MH2       ocessor ID     306c3   
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Fi: General Help P2: Previous Values F3: Optimized Defaults F4: Save & Exit Esc: Exit	NOD Vendor ND Version     IEI E434ER16.bin       PMI Module     N/A       Processor Information ame     Haswell Intel(R) Celeron(R) CPU 2800 MHz     ++: Select Screen 11: Select Item Enter: Select the select Enter: Select       Processor ID     306c3 COS	DD Vendor IEI DD Vension E434ER16.bin MI Module N/A ocessor Information me Haswell It: Select Screen 11: Select Item equency 2800 MHz eocessor ID 306c3 +/-: Change Opt. Forecasor ID 2006 Constraints photo Processors 2007 (2Thread(s) crocode Revision 1c to Not Applicable F3: Optimized Defaults F4: Save & Exit	NDD Vendor     IEI       NDD Version     E434ER16.bin       MI Module     N/A       mad String     Intel(R) Celeron(R) CPU       requency     2800 MHz       recessor ID     306c3       umber of Processors     2Core(s) / 2Thread(s)       Icrocode Revision     Ic       Ic Info     Not Applicable	DD Vendor IEI DD Version E434ER16.bin MI Module N/A accessor Information me Haswell and String Intel(R) Celeron(R) CPU equency 2800 MHz cocessor ID 306c3 epping C0 crocode Revision Ic crocode Revision Ic to for Module Filest Filest crocode Revision Ic Not Applicable Filest Haswell Haswell Haswell Haswell Haswell Haswell Haswell Haswell Haswell Haswell Haswell Hill Select Screen Haswell Hill Select Item Enter: Select Filest Filest Filest Filest Haswell Ha	0 Vendor       1E1         0 Version       E434ER16.bin         11 Module       N/A         icessor Information       ++: Select Screen         ie       Haswell         ind String       Intel(R) Celeron(R) CPU         iquency       2800 MHz         icessor ID       306c3         ipping       C0         ber of Processors       2Core(s) / 2Thread(s)         recode Revision       1c         Icf       Not Applicable	D Vendor IE1 D Version IE434ER16.bin H Module N/A Messor Information Messor Information Messor Information Messor Information Messor ID 2000 MHz Select Screen Haswell Messor ID 2000 MHz Correctly 2000 MHz File General Help File General	Do Vendor Do Version     IEi E434ER16.bin       MI Module     N/A       me and String     Intel(R) Celeron(R) CPU Equency       2800 MHz	NDD Vendor     1E1       NDD Version     E434ER16.bin       PMI Module     N/A       Processor Information     Haswell       and String     Intel(R) Celeron(R) CPU       requency     2800 MHz       requency     2800 MHz       retepping     CO       mober of Processors     2Corre(s) / 2Thread(s)	INDD Vendor     iEi       INDD Version     E434ER16.bin       INDD Version     E434ER16.bin       IPD Vendor     Haswell       Processor Information     Haswell       Inrand String     Intel(R) Celeron(R) CPU       Processor ID     306c3       Stepping     C0	pp Vendor iD Version II Module Module Module Module Maswell	pp Vendor iEi iD Version Haswell Md String and String Squency Squency 2800 MH2 2434ER16.bin Haswell	DD Vendor iEi DD Version E434ER16.bin MI Module N/A accessor Information me Haswell and String Intel(R) Celeron(R) CPU equency 2800 MHz eressor ID 3062 +/-: Change Opt.	by Vendor Dy Version     1E1 E434ER16.bin       Module     N/A       All Module     N/A       bccessor Information me     Haswell       and String     Intel(R) Celeron(R) CPU 2800 MHz       guency     2800 MHz       speeson TD     306c3	DD Vendor iE1 DD Version E434ER16.bin MI Hodule N/A ocessor Information me Haswell and String Intel(R) Celeron(R) CPU equency 2800 MHz excessor ID 3062 +/-: Change Opt.	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| T Tofo Not Applicable F4: Save & Exit  | T Info Not Applicable F4: Save & Exit<br>ESC: Exit   | T Info Not Applicable F4: Save & Exit<br>ESC: Exit  | T Info Not Applicable F4: Save & Exit<br>ESC: Exit   
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   | Info Not Applicable IF4: Save & Exit   |  | ICPUCUUE REVISION   |   | mber of Processors 2Core(s) / 2Thread(s) F2: Previous Values  
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   | FSC: Exit  |  |   | MICPOCOUE REVISION  | hber of Processors 2Core(s) / 2Thread(s) F2: Previous Values<br>rocode Revision 1c F3: Optimized Defaults   
   | hber of Processors 2Core(s) / 2Thread(s) F2: Previous Values<br>rocode Revision 1c F3: Optimized Defaults  | Deping         Corecision         ZDore(s) / 2Thread(s)         F2: Previous Values           mber of Processors         2Core(s) / 2Thread(s)         F3: Optimized Defaults           crocode Revision         1C         F3: Optimized Defaults   | wber of Processors 2Core(s) / 2Thread(s) F2: Previous Values<br>rocode Revision 1c F3: Optimized Defaults  | Bit Pop Ing         Core (s)         2 Thread(s)         F2: Previous Values           imber of Processors         2 Core (s)         2 Thread(s)         F3: Optimized Defaults           crocode Revision         10         F3: Optimized Defaults         F3: Optimized Defaults  | ber of Processors 2Core(s) / 2Thread(s) F2: Previous Values<br>recorde Revision 1c F3: Optimized Defaults  | Imber of Processors 2Core(s) / 2Thread(s) F2: Previous
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| N/A  | SEV VIETOS Version N/A   | CEV VETOS Version N/A   | ery version N/A  
  | N/A  |  |   |  
   |  | ESC: Exit  |   | To To Applicable F4: Save & Exit  | Spling         Core(s) / 2Thread(s)         F2: Previous Values           ber of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           procode Revision         1c         F3: Optimized Defaults           of n         Not Applicable         F4: Save & Exit  
   | Spling         Spling         F2: Previous Values           ber of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           procode Revision         1c         F3: Optimized Defaults           of n         Not Applicable         F4: Save & Exit   | Depuise         Core(s) / 2thread(s)         F2: Previous Values           crocode Revision         1c         F3: Optimized Defaults           tarfe         Not Applicable         F4: Save & Exit   | Opping         Core(s)         / 2Thread(s)         F2: Previous Values           procode Revision         1c         F3: Optimized Defaults           Info         Not Applicable         F4: Save & Exit   | Depuing         Document         F2: Previous Values           imber of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           icrocode Revision         1c         F3: Optimized Defaults           trafic         Not Applicable         F4: Save & Exit  | DDD         DDD         F2: Previous Values           ber of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           procode Revision         1c         F3: Optimized Defaults           Defa         Not Applicable         F4: Save & Exit   | Depuing         Document         F2: Previous Values  
        imbor of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           icrocode Revision         1c         F3: Optimized Defaults           to Tofo         Not Applicable         F4: Save & Exit  | DDD         DDD         F2: Previous Values           ber of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           procode Revision         1c         F3: Optimized Defaults           Defa         Not Applicable         F4: Save & Exit   | During         Core(s) / 2thread(s)         F2: Previous Values           crocode Revision         1c         F3: Optimized Defaults           tota         Not Applicable         F4: Save & Exit   | During         Core(s) / 2Thread(s)         F2: Previous Values           crocode Revision         1c         F3: Optimized Defaults           Infe         Not Applicable         F4: Save & Exit   | Epping         Core(s) / 2thread(s)         F2: Previous Values           crocode Revision         1c         F3: Optimized Defaults           Tofn         Not Applicable         F4: Save & Exit   | Epping         Core(s) / 2thread(s)         F2: Previous Values           crocode Revision         1c         F3: Optimized Defaults           Tofn         Not Applicable         F4: Save & Exit   
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  |  | SEV VRTOS Version N/H  | EX VETOS Version N/A  | V VRTOS Version N/A  
   |  | ESC: Exit  | ESC: Exit   | The Applicable F4: Save & Exit<br>ESC: Exit   | ppling     20pre(s) / 2Thread(s)     F2: Previous Values       procede Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit   
   | ppling     20pre(s) / 2Thread(s)     F2: Previous Values       ber of Processors     2Core(s) / 2Thread(s)     F3: optimized Defaults       procode Revision     1c     F3: optimized Defaults       Info     Not Applicable     F4: Save & Exit       ESC: Exit     ESC: Exit   | Pepping     2Core(s) / 2Thread(s)     F2: Previous Values       crocode Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit   | ppling     20pre(s) / 2Thread(s)     F2: Previous Values       procode Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit  | Depping     Core(s) / 2Thread(s)     F2: Previous Values       icrocode Revision     1c     F3: optimized Defaults       icrocode Revision     Not Applicable     F4: Save & Exit       ESC: Exit     ESC: Exit   | Diffy         Core(s) / 2Thread(s)         F2: Previous Values           processors         2Core(s) / 2Thread(s)         F3: Optimized Defaults           procode Revision         1c         F3: Optimized Defaults           Info         Not Applicable         F4: Save & Exit           ESC: Exit         ESC: Exit              | Depping     Core(s) / 2Thread(s)     F2: Previous
Values       Icrocode Revision     1c     F3: Optimized Defaults       Icrocode Revision     Not Applicable     F4: Save & Exit       ESC: Exit     ESC: Exit  | Diffy         Core(s) / 2Thread(s)         F2: Previous Values           processors         2Core(s) / 2Thread(s)         F3: Optimized Defaults           procode Revision         1c         F3: Optimized Defaults           Info         Not Applicable         F4: Save & Exit           ESC: Exit         ESC: Exit              | Pupping     2Core(s) / 2Thread(s)     F2: Previous Values       mober of Processors     2Core(s) / 2Thread(s)     F3: Optimized Defaults       crocode Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit       ESC: Exit     ESC: Exit  | Pupping     20ore(s) / 2Thread(s)     F2: Previous Values       moer of Processors     2Core(s) / 2Thread(s)     F3: Optimized Defaults       crocode Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit       ESC: Exit     ESC: Exit   | Pupping     2Core(s) / 2Thread(s)     F2: Previous Values       crocode Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit   | Pupping     2Core(s) / 2Thread(s)     F2: Previous Values       crocode Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit  
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|  |  | amoru RC Version 1.6.2.1  |  
  | IN VEIGO VEI SIGN  |  |   | X VD100 VCI S10II  
   | TX VBIOS Version N/A   | FX VBIOS Version N/A   | GFX VBIOS Version N/A   | IGF VBIDS Version N/A Applicable F4: Save & Exit<br>ESC: Exit   | Spling     2Core(s) / 2Thread(s)     F2: provious Values       Spocde Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit       EX VBIOS Version     N/A     F3: Optimized Defaults  
   | Spling     Core(s) / 2Thread(s)     F2: Previous Values       Spocede Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit       EX VBIOS Version     N/A     F3: Optimized Defaults   | Eppling         Core(s) / 2Thread(s)         F2: Previous Values           crocode Revision         1c         F3: Optimized Defaults           fair         Not Applicable         F4: Save & Exit           FX VBIOS Version         N/A         F4: Save & Exit   | Dpling     Core(s) / 2Thread(s)     F2: Previous Values       procede Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit       EX VBIOS Version     N/A     F3   | Epping         Core(s)         ZThread(s)         F2: Previous Values           icrocode Revision         1c         F3: Optimized Defaults         F3: Optimized Defaults           icrocode Revision         Not Applicable         F4: Save & Exit         ESC: Exit   | During         Openation         P2: previous Values           ber of Processors         2Core(s) / 2Thread(s)         F2: previous Values           procode Revision         1c         F3: Dotimized Defaults           Info         Not Applicable         F4: Save & Exit           EX VBIOS Version         N/A         ESC: Exit | Depuis         Core(s)         Z Thread(s)         F2:
Previous Values           icrocode Revision         1c         F3: Optimized Defaults         F3: Optimized Defaults           Info         Not Applicable         F4: Save & Exit         ESC: Exit           SFX VBIOS Version         N/A         F4: Save & Exit         F4: Save & Exit  | During         Openation         P2: previous Values           ber of Processors         2Core(s) / 2Thread(s)         F2: previous Values           procode Revision         1c         F3: Dotimized Defaults           Info         Not Applicable         F4: Save & Exit           EX VBIOS Version         N/A         ESC: Exit | Epping         Core(s)         2 Thread(s)         F2: Previous Values           moer of Processors         1c         F3: DotImized Defaults         F4: Save & Exit           Info         Not Applicable         F4: Save & Exit         ESC: Exit  | Expline     Core(s) / 2Thread(s)     F2: Previous Values       forcessors     2Core(s) / 2Thread(s)     F3: DotImized Defaults       crocode Revision     1c     F4: Save & Exit       Info     Not Applicable     F4: Save & Exit       FX VBIOS Version     N/A     F5   | Ending         Core(s) / 2Thread(s)         F2: Previous Values           moer of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           crocode Revision         1c         F3: Optimized Defaults           Info         Not Applicable         F4: Save & Exit           FX VBIOS Version         N/A         F4: Save & Exit   | Ending         Core(s) / 2Thread(s)         F2: Previous Values           moer of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           crocode Revision         1c         F3: Optimized Defaults           Info         Not Applicable         F4: Save & Exit           FX VBIOS Version         N/A         F4: Save & Exit   
   |
| N/A  | SEX VBIOS Version N/A  | GFX VBIOS Version N/A   | EX VETOS Vension N/A   
  | N/A  |  |   | ESC: EXIT  
   | ESC: Exit  |  | IC COUCE REVISION   | Number of Processors 200 C(3) / Enhoused of faults  | |
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  | IN VEIGO VEI SIGN  |  |   | X VD100 VCI S10II  
   | TX VBIOS Version N/A   | FX VBIOS Version N/A   | GFX VBIOS Version N/A   | T Info Not Applicable F4: Save & Exit<br>ESC: Exit<br>SFX VBIOS Version N/A   | Spling     2Core(s) / 2Thread(s)     F2: provious Values       Spocde Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit       EX VBIOS Version     N/A     F3: Optimized Defaults  
   | Spling     Core(s) / 2Thread(s)     F2: Previous Values       Spocede Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit       EX VBIOS Version     N/A     F3: Optimized Defaults   | Eppling         Core(s) / 2Thread(s)         F2: Previous Values           crocode Revision         1c         F3: Optimized Defaults           fair         Not Applicable         F4: Save & Exit           FX VBIOS Version         N/A         F4: Save & Exit   | Dpling     Core(s) / 2Thread(s)     F2: Previous Values       procede Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit       EX VBIOS Version     N/A     F3   | Epping         Core(s)         ZThread(s)         F2: Previous Values           icrocode Revision         1c         F3: Optimized Defaults         F3: Optimized Defaults           icrocode Revision         Not Applicable         F4: Save & Exit         ESC: Exit   | During         Openation         P2: previous Values           ber of Processors         2Core(s) / 2Thread(s)         F2: previous Values           procode Revision         1c         F3: Dotimized Defaults           Info         Not Applicable         F4: Save & Exit           EX VBIOS Version         N/A         ESC: Exit | Depuis         Core(s)         Z Thread(s)         F2:
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|  |  | amory RC Version 1.6.2.1  |  
  | IN VEIGO VEI SIGN  |  |   | X VD100 VCI S10II  
   | TX VBIOS Version N/A   | FX VBIOS Version N/A   | GFX VBIOS Version N/A   | IST Info Not Applicable F4: Save & Exit<br>ESC: Exit  | Spling     2Core(s) / 2Thread(s)     F2: provious Values       Spocde Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit       EX VBIOS Version     N/A     F3: Optimized Defaults  
   | Spling     Core(s) / 2Thread(s)     F2: Previous Values       Spocede Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit       EX VBIOS Version     N/A     F3: Optimized Defaults   | Eppling         Core(s) / 2Thread(s)         F2: Previous Values           crocode Revision         1c         F3: Optimized Defaults           fair         Not Applicable         F4: Save & Exit           FX VBIOS Version         N/A         F4: Save & Exit   | Dpling     Core(s) / 2Thread(s)     F2: Previous Values       procede Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit       EX VBIOS Version     N/A     F3   | Epping         Core(s)         ZThread(s)         F2: Previous Values           icrocode Revision         1c         F3: Optimized Defaults         F3: Optimized Defaults           icrocode Revision         Not Applicable         F4: Save & Exit         ESC: Exit   | During         Openation         P2: previous Values           ber of Processors         2Core(s) / 2Thread(s)         F2: previous Values           procode Revision         1c         F3: Dotimized Defaults           Info         Not Applicable         F4: Save & Exit           EX VBIOS Version         N/A         ESC: Exit | Depuis         Core(s)         Z Thread(s)         F2:
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| N/A  | AFX VBIOS Version N/A  | GFX VBIOS Version N/A   | EEV VETOS Vension N/A  
  | N/A  |  |   | ESC: EXIT  
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|  | AFX VBTOS Version N/H  | GFX VBIOS Version N/H   |  
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   |  | ESC: Exit  | ESC: Exit   | The Applicable F4: Save & Exit<br>ESC: Exit   | ppling     20pre(s) / 2Thread(s)     F2: Previous Values       procede Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit   
   | ppling     20pre(s) / 2Thread(s)     F2: Previous Values       ber of Processors     2Core(s) / 2Thread(s)     F3: optimized Defaults       procode Revision     1c     F3: optimized Defaults       Info     Not Applicable     F4: Save & Exit       ESC: Exit     ESC: Exit   | Pepping     2Core(s) / 2Thread(s)     F2: Previous Values       crocode Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit   | ppling     20pre(s) / 2Thread(s)     F2: Previous Values       procode Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit  | Depping     Core(s) / 2Thread(s)     F2: Previous Values       icrocode Revision     1c     F3: optimized Defaults       icrocode Revision     Not Applicable     F4: Save & Exit       ESC: Exit     ESC: Exit   | Diffy         Diffy         F2: Previous Values           ber of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           procode Revision         1c         F3: Optimized Defaults           Info         Not Applicable         F4: Save & Exit           ESC: Exit         ESC: Exit                         | Depping     Core(s) / 2Thread(s)     F2: Previous
Values       Icrocode Revision     1c     F3: Optimized Defaults       Icrocode Revision     Not Applicable     F4: Save & Exit       ESC: Exit     ESC: Exit  | Diffy         Diffy         F2: Previous Values           ber of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           procode Revision         1c         F3: Optimized Defaults           Info         Not Applicable         F4: Save & Exit           ESC: Exit         ESC: Exit                         | Pupping     2Core(s) / 2Thread(s)     F2: Previous Values       mober of Processors     2Core(s) / 2Thread(s)     F3: Optimized Defaults       crocode Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit       ESC: Exit     ESC: Exit  | Pupping     20ore(s) / 2Thread(s)     F2: Previous Values       moer of Processors     2Core(s) / 2Thread(s)     F3: Optimized Defaults       crocode Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit       ESC: Exit     ESC: Exit   | Pupping     2Core(s) / 2Thread(s)     F2: Previous Values       crocode Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit   | Pupping     2Core(s) / 2Thread(s)     F2: Previous Values       crocode Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit  
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   | TX VBIOS Version N/A   | FX VBIOS Version N/A   | GFX VBIOS Version N/A   | IGFOLDE REVISION IN A Applicable F4: Save & Exit<br>ESC: Exit<br>IGFX VBIDS Version N/A   | Spling     2Core(s) / 2Thread(s)     F2: previous Values       Scorede Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit       EX VBIOS Version     N/A     F3: Optimized Defaults   
   | Spling     Core(s) / 2Thread(s)     F2: Previous Values       Scored Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit       EX VBIOS Version     N/A     VA  | Epping     Core(s) / 2Thread(s)     F2: Previous Values       crocode Revision     1c     F3: Optimized Defaults       Thro     Not Applicable     F4: Save & Exit       FX VBIOS Version     N/A     F3: Optimized Defaults   | Dpling     Core(s) / 2Thread(s)     F2: Previous Values       procede Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit       EX     VBIOS Version     N/A  | Epping     Core(s) / 2Thread(s)     F2: Previous Values       icrocode Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit       SFX VBIOS Version     N/A     F3: Save Applicable   | http://publication     P2: previous Values       ber of Processors     2Core(s) / 2Thread(s)       procode Revision     1c       Info     Not Applicable       P4: Save & Exit       ESC: Exit   | Depuis         Correc(s) / 2Thread(s)         F2:
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  | IN VEIGO VEI SIGN  |  |   | X VD100 VCI S10II  
   | TX VBIOS Version N/A   | FX VBIOS Version N/A   | GFX VBIOS Version N/A   | T Info Not Applicable F4: Save & Exit<br>ESC: Exit<br>SFX VBIOS Version N/A   | Spling     2Core(s) / 2Thread(s)     F2: provious Values       Spocde Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit       EX VBIOS Version     N/A     F3: Optimized Defaults  
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  | TEX VBIUS Version  | SFX VBIOS Version N/H  | FX VBIOS Version N/A  | X VBIOS Version N/A  
   |  | ESC: Exit  | ESC: Exit   | T Info Not Applicable F4: Save & Exit<br>ESC: Exit  | ppling     20pre(s) / 2Thread(s)     F2: Previous Values       procede Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit   
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  |  | SEX VBIOS Version N/H  | EX VBIDS Version N/A  | X VBTOS Version N/A  
   |  | ESC: Exit  | ESC: Exit   | T Info Not Applicable F4: Save & Exit<br>ESC: Exit  | ppling     20pre(s) / 2Thread(s)     F2: Previous Values       procede Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit   
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|  |  | GEX VBIUS VERSION NZH   |  
  |  |  |   | N/A  
   |  | ESC: Exit  | ESC: Exit   | T Info Not Applicable F4: Save & Exit<br>ESC: Exit  | ppling     20pre(s) / 2Thread(s)     F2: Previous Values       procede Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit   
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Values       Icrocode Revision     1c     F3: Optimized Defaults       Icrocode Revision     Not Applicable     F4: Save & Exit       ESC: Exit     ESC: Exit  | Diffy         Core(s) / 2Thread(s)         F2: Previous Values           processors         2Core(s) / 2Thread(s)         F3: Optimized Defaults           procode Revision         1c         F3: Optimized Defaults           Info         Not Applicable         F4: Save & Exit           ESC: Exit         ESC: Exit              | Pupping     2Core(s) / 2Thread(s)     F2: Previous Values       mober of Processors     2Core(s) / 2Thread(s)     F3: Optimized Defaults       crocode Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit       ESC: Exit     ESC: Exit  | Pupping     20ore(s) / 2Thread(s)     F2: Previous Values       moer of Processors     2Core(s) / 2Thread(s)     F3: Optimized Defaults       crocode Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit       ESC: Exit     ESC: Exit   | Pupping     2Core(s) / 2Thread(s)     F2: Previous Values       crocode Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit   | Pupping     2Core(s) / 2Thread(s)     F2: Previous Values       crocode Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit  
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  |  |  | EX VETOS Version N/A  | V VRTOS Version N/A  
   |  | ESC: Exit  | ESC: Exit   | T Info Not Applicable F4: Save & Exit<br>ESC: Exit  | ppling     20pre(s) / 2Thread(s)     F2: Previous Values       procede Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit   
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|  |  | AFX VETUS VERSION N/H   |  
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   |  | ESC: Exit  | ESC: Exit   | r Info Not Applicable F4: Save & Exit<br>ESC: Exit  | ppling     20pre(s) / 2Thread(s)     F2: Previous Values       procede Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit   
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| N/A  | SEX VRIOS Version N/A  | SEX VBIOS Version N/A   | N/A N/A  
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   | Loo. Lott  | ESC: Exit  |   | F4: Save & Exit   | Spling         Core(s) / 2Thread(s)         F2: Previous Values           ber of Processors         2Core(s) / 2Thread(s)         F2: Optimized Defaults           procode Revision         1c         F3: Optimized Defaults           Info         Not Applicable         F4: Save & Exit   
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| N/A  | SEX VBIOS Version N/A  | GFX VBIOS Version N/A   | CEV VETOS Vension N/A  
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   |  | ESC: Exit  | ESC: Exit   | T Info Not Applicable F4: Save & Exit<br>ESC: Exit  | ppling     20ore(s) / 2Thread(s)     F2: Previous Values       ber of Processors     2Core(s) / 2Thread(s)     F3: optimized Defaults       procode Revision     1c     F3: optimized Defaults       Info     Not Applicable     F4: Save & Exit       ESC: Exit     ESC: Exit  
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| AFY UPTOR Vancion N/A  | SEX VBIOS Version N/A  | REX VBIOS Version N/A   | CEX VETOS Version N/A  
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| Inner of Processors 2001 (1977) Enhanced (1977)<br>icrocode Revision 1c F3: Optimized Defaults<br>Toric Not Applicable F4: Save & Exit   | Inder of Processors Lobic(S) / Enh Cad(S) Primized Defaults<br>Erocode Revision 1c Pri Optimized Defaults<br>T Info Not Applicable Pri: Save & Exit<br>ESC: Exit   | Under of Processors Econectory Enhanced of Priorestand Definitized Defaults<br>incroade Revision ic Priorestand Defaults<br>T Info Not Applicable Fri: Save & Exit<br>ESC: Exit   | Inder of Processors 2 concerns 1   | Information Sector Sect   | Information of Processors 200 cts) / Enhanced to F3: Optimized Defaults F3: Optimized Defaults F4: Save & Exit F4: Save & Exit   | crocode Revision     1c     F3: Optimized Defaults     F4: Save & Exit  | incert of Processon's 200 cts) / Enh codits) / Princed Cts /   | procode Revision 10 F3: Optimized Defaults   | crocode Revision 1c F3: Optimized Defaults   | umber of Processors 200 ets) / Enh date,  |   |   | co E0  | eppind CO IFI: General help  | apping EO EFI: General netp  | enning E0   | EO EO  | liu III. deneral netp  |  |  |  |  |  |
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| Lepping         Core(s) / 2Thread(s)         F2: Previous Values           umber of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           icrocode Revision         1c         F3: Optimized Defaults           T Info         Not Applicable         F4: Save 8 Exit   | Uterpling     Documents     F2: Previous Values       umber of Processors     2Core(s) / 2Thread(s)     F2: Optimized Defaults       icrocode Revision     1c     F3: Optimized Defaults       T Info     Not Applicable     F4: Save & Exit       ESC: Exit     ESC: Exit   | Uterpring     200re(s) / 2Thread(s)     F2: Previous Values       umber of Processors     2Core(s) / 2Thread(s)     F3: Optimized Defaults       icrocode Revision     1c     F3: Optimized Defaults       T Info     Not Applicable     F4: Save & Exit       ESC: Exit     ESC: Exit  | Uterpling     Do     200re(S) / 2Thread(S)     F2: Previous Values       icrocode Revision     1c     F3: Optimized Defaults       T Info     Not Applicable     F4: Save & Exit   
  | Depting         Core(s)         Zthread(s)         F2: Previous Values           icrocode Revision         1c         F3: Optimized Defaults           icrocode Revision         Not Applicable         F4: Save & Exit  | Depuips         Decore(s) / 2Thread(s)         F2: Previous Values           icrocode Revision         1c         F3: Optimized Defaults           traff         Save & Exit   | Epping         Core(s) / 2Thread(s)         F2: Provious Values           mober of Processors         2Core(s) / 2Thread(s)         F3: Optimized Defaults           crocode Revision         1c         F3: Optimized Defaults           Tota         Not Applicable         F4: Save & Exit   | Dimy         Core(s) / 2Thread(s)         F2: Previous Values           procede Revision         1c         F3: optimized Defaults           Toto         Not Applicable         F4: Save & Exit   
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| CO     CO     F1: General Help       mber of Processors     2Core(s) / 2Thread(s)     F2: Previous Values       crocode Revision     1c     F3: Optimized Defaults       Tota     Not Applicable     F4: Save & Exit   | epping Co<br>mber of Processors 2Core(s) / 2Thread(s) F1: General Help<br>rococde Revision 1c<br>Info Not Applicable F3: Optimized Defaults<br>F4: Save & Exit<br>ESD: Exit  | epping Co<br>mber of Processors 2Core(s) / 2Thread(s) F1: General Help<br>rococde Revision 1c<br>Info Not Applicable F3: Optimized Defaults<br>F4: Save & Exit<br>ESD: Exit   | Pl: General Help<br>epping<br>mber of Processors<br>crocode Revision<br>' Info<br>Not Applicable<br>ESC: Exit   | CO     CO     F1: General Help       mber of Processors     2Core(s) / 2Thread(s)     F2: Previous Values       crocode Revision     1c     F3: Optimized Defaults       Tota     Not Applicable     F4: Save & Exit   | CO     CO     F1: General Help       mber of Processors     2Core(s) / 2Thread(s)     F2: Previous Values       crocode Revision     1c     F3: Optimized Defaults       Tota     Not Applicable     F4: Save & Exit   | CO     CO     F1: General Help       mber of Processors     2Core(s) / 2Thread(s)     F2: Previous Values       crocode Revision     1c     F3: Optimized Defaults       Tota     Not Applicable     F4: Save & Exit  | Absolution     CO     F1: General Help       mber of Processors     2Core(s) / 2Thread(s)     F2: Previous Values       procode Revision     1c     F3: Optimized Defaults       Info     Not Applicable     F4: Save & Exit   | pping Correction Correction Correction Correction Correct Correction Correcti   | Docusion is         Docusion         File General Help           mber of Processors         2Core(s) / 2Thread(s)         File General Help           crocode Revision         1c         File General Help           F3: Corela Laboration         File General Help         File General Help  | epping C0 F1: General Help F2: Previous Values F2: Previous Values  | Ecpling CO F1: General Help   |   |  |  |  |   |  |  |  |  |  |  |  |
| Outpession         District         Fit:         General         Help           upping         C0         Fit:         General         Help           jmber of         Processors         2Core(s) / 2Thread(s)         F2t:         Previous         Values           icrocode         Revision         1c         F3t:         Optimized         Defaults           t         Torfo         Not         Applicable         F4t:         Save & Exit  | Butesson 10     Document     Fit General Help       Lepping     Cone     Fit General Help       Imber of Processors     2Core(s) / 2Thread(s)     Fit Previous Values       Ecrocode Revision     1c     Fit Save & Exit       T Info     Not Applicable     Fit Save & Exit       ESC: Exit     ESC: Exit     Fit Save & Exit   | Butesson 10     Document     Fit General Help       Lepping     Cone     Fit General Help       Imber of Processors     2Core(s) / 2Thread(s)     Fit Previous Values       Ecrocode Revision     1c     Fit Save & Exit       T Info     Not Applicable     Fit Save & Exit       ESC: Exit     ESC: Exit     Fit Save & Exit  | Buttlesson 10     Doublesson 10     Fit General Help       Lepping     Co     Fit General Help       Imber of Processors     2Core(s) / 2Thread(s)     Fit Previous Values       Lorocode Revision     1c     Fit General Help       T Info     Not Applicable     Fit Save 8 Exit       ESC: Exit     ESC: Exit     Esc: Exit   
  | Outcostor         Discostor         F1: General Help           imber of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           icrocode Revision         1c         F3: Optimized Defaults           t Tafo         Not Applicable         F4: Save & Exit   | Outpession         District         Fit:         General         Help           upping         C0         Fit:         General         Help           jmber of         Processors         2Core(s) / 2Thread(s)         F2t:         Previous         Values           icrocode         Revision         1c         F3t:         Optimized         Defaults           t         Info         Not         Applicable         F4t:         Save & Exit   | Duession ID         Dockson         F1: General Help           epping         C0         F1: General Help           mber of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           crocode Revision         1c         F3: Optimized Defaults           tota         Not Applicable         F4: Save & Exit   | Description         Dockson         F1: General Help           pping         C0         F1: General Help           wher of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           procode Revision         1c         F3: Optimized Defaults           Defa         Not Applicable         F4: Save & Exit   
   | Direction         Direction         File General Help           pping         CO         File General Help           wber of Processors         2Core(s) / 2Thread(s)         File General Help           procode Revision         1c         File General Help           F3: General Help         File General Help         File General Help   | Debsion ID Docket FileGeneral Help<br>mber of Processors 2Core(s) / 2Thread(s) F2: Previous Values<br>crocode Revision 1c F3: Optimized Defaults   | tepping C0 F1: General Help<br>maker of Processors 2Core(s) / 2Thread(s) F2: Previous Values  | tepping CO F1: General Help   |   
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| Outestor         District         F1: General Help           epping         C0         F1: General Help           mber of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           crocode Revision         1c         F3: Optimized Defaults           Tota         Not Applicable         F4: Save & Exit  | blesson iD<br>pping<br>mber of Processors<br>crocode Revision<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info  | blesson iD<br>pping<br>mber of Processors<br>crocode Revision<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info<br>Info   | Ducesion iD     Dock     File General Help       epping     CD     F2: Previous Values       mber of Processors     2Core(s) / 2Thread(s)     F2: Previous Values       crocode Revision     1c     F3: Optimized Defaults       'Info     Not Applicable     F4: Save & Exit       ESC: Exit     ESC: Exit  
  | Outestor         District         F1: General Help           epping         C0         F1: General Help           mber of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           crocode Revision         1c         F3: Optimized Defaults           Tota         Not Applicable         F4: Save & Exit  | Outestor         District         F1: General Help           epping         C0         F1: General Help           mber of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           crocode Revision         1c         F3: Optimized Defaults           Tofn         Not Applicable         F4: Save & Exit  | Duession ID         Dockson         F1: General Help           epping         C0         F1: General Help           mber of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           crocode Revision         1c         F3: Optimized Defaults           tota         Not Applicable         F4: Save & Exit   | Descent         Discourse         F1: General Help           pping         CO         F1: General Help           wher of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           procode Revision         1c         F3: Optimized Defaults           Defa         Not Applicable         F4: Save & Exit   
   | Direction         Direction         F1: General Help           pping         C0         F2: Previous Values           wber of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           procode Revision         1c         F3: Optimized Defaults  | Duession ID         Double         File General Help           epping         CO         File General Help           mber of Processors         2Core(s) / 2Thread(s)         File General Help           crocode Revision         1c         File General Help           F3: Optimized Defaults         F3: Optimized Defaults  | epping C0 F1: General Help<br>mber of Processors 2Core(s) / 2Thread(s) F2: Previous Values  | epping CO F1: General Help  |   
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| Outpoint         District         Fit: General Help           umber of Processors         2Core(s) / 2Thread(s)         Fit: Previous Values           icrocode Revision         1c         Fit: Optimized Defaults           1         Tunfo         Not Applicable         F4t: Save & Exit  | Understand     D     D     Fit General Helo       Lepping     Co     Fit General Helo       Jimber of Processors     2Core(s) / 2Thread(s)     Fit Previous Values       Licrocode Revision     1c     Fit Optimized Defaults       T Info     Not Applicable     Fit Save & Exit       ESC: Exit     ESC: Exit  | Understand     D     D     Fit General Helo       Lepping     Co     Fit General Helo       Jimber of Processors     2Core(s) / 2Thread(s)     Fit Previous Values       Licrocode Revision     1c     Fit Optimized Defaults       T Info     Not Applicable     Fit Save & Exit       ESC: Exit     ESC: Exit   | Understor 10     Double     Fit General Help       pumber of Processors     2Core(s) / 2Thread(s)     F2: Previous Values       Licrocode Revision     1c     F3: Optimized Defaults       T Info     Not Applicable     F4: Save & Exit       ESC: Exit     ESC: Exit     F3: Optimized Defaults  
  | Outcostor         Discostor         F1: General Help           imber of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           icrocode Revision         1c         F3: Optimized Defaults           It Tafo         Not Applicable         F4: Save & Exit  | Outpession         District         Fit:         General         Help           pumber of         Processors         2Core(s) / 2Thread(s)         Fit:         Fit:         Previous         Values           icrocode         Revision         1c         Fit:         Optimized         Defaults           icrocode         Not         Applicable         F4t:         Save & Exit   | Duession ID         Dockson         F1: General Help           epping         C0         F1: General Help           mber of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           crocode Revision         1c         F3: Optimized Defaults           tota         Not Applicable         F4: Save & Exit   | Descent         Discourse         F1: General Help           pping         CO         F1: General Help           wher of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           procode Revision         1c         F3: Optimized Defaults           Defa         Not Applicable         F4: Save & Exit   
   | Direction         Direction         F1: General Help           pping         C0         F2: Previous Values           wber of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           procode Revision         1c         F3: Optimized Defaults  | Duession ID         Double         File General Help           epping         CO         File General Help           mber of Processors         2Core(s) / 2Thread(s)         File General Help           crocode Revision         1c         File General Help           F3: Optimized Defaults         F3: Optimized Defaults  | tepping C0 F1: General Help F2: Previous Values F2: Previous Values   | tepping CO F1: General Help   |   
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| Duession ID         Dockson         F1: General Help           epping         C0         F1: General Help           mber of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           crocode Revision         1c         F3: Optimized Defaults           Tota         Not Applicable         F4: Save & Exit  | Duession ID     Double       Deping     Co       mber of Processors     2Core(s) / 2Thread(s)       F1: General Help       recorde Revision     1c       Info     Not Applicable       F4: Save & Exit       ESC: Exit   | Duession ID     Double       Deping     Co       mber of Processors     2Core(s) / 2Thread(s)       F1: General Help       recorde Revision     1c       Info     Not Applicable       F4: Save & Exit       ESC: Exit  | Duession ID     Double Source     File General Help       mober of Processors     2Core(s) / 2Thread(s)     File General Help       crocode Revision     1c     File General Help       Tinfo     Not Applicable     File General Help       ESC:     Exit     Exit  
  | Duession 1D         Dockson         F1: General Help           epping         C0         F1: General Help           mber of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           crocode Revision         1c         F3: Optimized Defaults           Tota         Not Applicable         F4: Save & Exit  | Duession 1D         Dockson         F1: General Help           epping         C0         F1: General Help           mber of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           crocode Revision         1c         F3: Optimized Defaults           Tota         Not Applicable         F4: Save & Exit  | Duession ID         Dockson         F1: General Help           epping         C0         F1: General Help           mber of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           crocode Revision         1c         F3: Optimized Defaults           tota         Not Applicable         F4: Save & Exit   | Description         Dockson         F1: General Help           pping         C0         F1: General Help           wher of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           procode Revision         1c         F3: Optimized Defaults           Defa         Not Applicable         F4: Save & Exit   
   | Direction         Direction         File General Help           pping         CO         File General Help           wber of Processors         2Core(s) / 2Thread(s)         File General Help           procode Revision         1c         File General Help           F3: General Help         File General Help         File General Help   | Debsion ID Docket FileGeneral Help<br>mber of Processors 2Core(s) / 2Thread(s) F2: Previous Values<br>crocode Revision 1c F3: Optimized Defaults   | epping C0 F1: General Help F2: Previous Values Walles   | epping CO F1: General Help  |   
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| Objectson 10     Discussion     F1: General Help       umber of Processors     2Core(s) / 2Thread(s)     F2: Previous Values       icrocode Revision     1c     F3: Optimized Defaults       Icrocode Revision     Not Applicable     F4: Save & Exit  | Understand     Document     Fit General Help       Lepping     Cone     Fit General Help       Jumber of Processors     2Core(s) / 2Thread(s)     Fit Previous Values       Licrocode Revision     1c     Fit Save & Exit       T Info     Not Applicable     Fit Save & Exit  | Understand     Document     Fit General Help       Lepping     Cone     Fit General Help       Jumber of Processors     2Core(s) / 2Thread(s)     Fit Previous Values       Licrocode Revision     1c     Fit Save & Exit       T Info     Not Applicable     Fit Save & Exit   | Understor 10     Double     Fit General Help       pumber of Processors     2Core(s) / 2Thread(s)     F2: Previous Values       Licrocode Revision     1c     F3: Optimized Defaults       T Info     Not Applicable     F4: Save & Exit       ESC: Exit     ESC: Exit     F3: Optimized Defaults  
  | Outcostor         Discostor         F1: General Help           imber of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           icrocode Revision         1c         F3: Optimized Defaults           It Tafo         Not Applicable         F4: Save & Exit  | Outpession         District         Fit:         General         Help           pumber of         Processors         2Core(s) / 2Thread(s)         Fit:         Fit:         Previous         Values           icrocode         Revision         1c         Fit:         Optimized         Defaults           icrocode         Not         Applicable         F4t:         Save & Exit   | Duession ID         Dockson         F1: General Help           epping         C0         F1: General Help           mber of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           crocode Revision         1c         F3: Optimized Defaults           tota         Not Applicable         F4: Save & Exit   | Descent         Discourse         F1: General Help           pping         CO         F1: General Help           wher of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           procode Revision         1c         F3: Optimized Defaults           Defa         Not Applicable         F4: Save & Exit   
   | Direction         Direction         F1: General Help           pping         C0         F2: Previous Values           wber of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           procode Revision         1c         F3: Optimized Defaults  | Duession ID         Dock         F1: General Help           epping         C0         F1: General Help           mber of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           crocode Revision         1c         F3: Optimized Defaults   | tepping C0 F1: General Help F2: Previous Values F2: Previous Values   | tepping CO F1: General Help   |   
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| Outpessor     District       tapping     C0       umber of Processors     2Core(s) / 2Thread(s)       firecode Revision     1c       to tapping     Not Applicable       F4: Save & Exit   | Understand     D     D     Fit General Helo       Lepping     Co     Fit General Helo       Jimber of Processors     2Core(s) / 2Thread(s)     Fit Previous Values       Licrocode Revision     1c     Fit Optimized Defaults       T Info     Not Applicable     Fit Save & Exit       ESC: Exit     ESC: Exit  | Outcostor 10     Discostor     Fit General Helo       umber of Processors     2Core(s) / 2Thread(s)     F2: Previous Values       icrocode Revision     1c     F3: Optimized Defaults       T Info     Not Applicable     F4: Save & Exit   | Understor 10     Double     Fit General Help       pumber of Processors     2Core(s) / 2Thread(s)     F2: Previous Values       Licrocode Revision     1c     F3: Optimized Defaults       T Info     Not Applicable     F4: Save & Exit       ESC: Exit     ESC: Exit     F3: Optimized Defaults  
  | Outcostor         Discostor         F1: General Help           imber of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           icrocode Revision         1c         F3: Optimized Defaults           It Tafo         Not Applicable         F4: Save & Exit  | Outpession         District         Fit:         General         Help           pumber of         Processors         2Core(s) / 2Thread(s)         Fit:         Fit:         Previous         Values           icrocode         Revision         1c         Fit:         Optimized         Defaults           icrocode         Not         Applicable         F4t:         Save & Exit   | Duession ID         Dockson         F1: General Help           epping         C0         F1: General Help           mber of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           crocode Revision         1c         F3: Optimized Defaults           tota         Not Applicable         F4: Save & Exit   | Descent         Discourse         F1: General Help           pping         CO         F1: General Help           wher of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           procode Revision         1c         F3: Optimized Defaults           Defa         Not Applicable         F4: Save & Exit   
   | Direction         Direction         F1: General Help           pping         C0         F2: Previous Values           wber of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           procode Revision         1c         F3: Optimized Defaults  | Duession ID         Double         File General Help           epping         CO         File General Help           mber of Processors         2Core(s) / 2Thread(s)         File General Help           crocode Revision         1c         File General Help           F3: Optimized Defaults         F3: Optimized Defaults  | tepping C0 F1: General Help F2: Previous Values F2: Previous Values   | tepping CO F1: General Help   |   
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| Outpession         District         Fit:         General         Help           pumber of         Processors         2Core(s) / 2Thread(s)         Fit:         Fit:         Previous         Values           icrocode         Revision         1c         Fit:         Optimized         Defaults           icrocode         Not         Applicable         F4t:         Save & Exit   | Butesson 10     Color     Fit General Help       Lepping     Color     Fit General Help       Imber of Processors     2Core(s) / 2Thread(s)     Fit Previous Values       Ecrocode Revision     1c     Fit Save & Exit       T Info     Not Applicable     Fit Save & Exit   | Butesson 10     Color     Fit General Help       Lepping     Color     Fit General Help       Imber of Processors     2Core(s) / 2Thread(s)     Fit Previous Values       Ecrocode Revision     1c     Fit Save & Exit       T Info     Not Applicable     Fit Save & Exit  | Buttlesson 10     Doublesson 10     Fit General Help       Lepping     C0     F2: Previous Values       Imber of Processors     2Core(s) / 2Thread(s)     F2: Previous Values       Errocode Revision     1c     F3: Optimized Defaults       T Info     Not Applicable     F4: Save & Exit       ESC: Exit     Esc: Exit  
  | Outcostor         Discostor         F1: General Help           imber of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           icrocode Revision         1c         F3: Optimized Defaults           It Tafo         Not Applicable         F4: Save & Exit  | Outperson     District       repping     C0       jimber of Processors     2Core(s) / 2Thread(s)       revision     1c       icrocode Revision     1c       row     Not Applicable       F4: Save & Exit   | Duession ID         Dockson         F1: General Help           epping         C0         F1: General Help           mber of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           crocode Revision         1c         F3: Optimized Defaults           tota         Not Applicable         F4: Save & Exit   | Descent         Discourse         F1: General Help           pping         CO         F1: General Help           wher of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           procode Revision         1c         F3: Optimized Defaults           Defa         Not Applicable         F4: Save & Exit   
   | Direction         Direction         F1: General Help           pping         C0         F2: Previous Values           wber of Processors         2Core(s) / 2Thread(s)         F2: Previous Values           procode Revision         1c         F3: Optimized Defaults  | Duession ID         Double         File General Help           epping         CO         File General Help           mber of Processors         2Core(s) / 2Thread(s)         File General Help           crocode Revision         1c         File General Help           F3: Optimized Defaults         F3: Optimized Defaults  | tepping C0 F1: General Help F2: Previous Values F2: Previous Values   | tepping CO F1: General Help   |   
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  |  | UCESSOF ID   | DUESSON ID   | UCESSOF ID   | UCESSOF ID   
   |
| Outpession         District         Fit:         General         Help           upping         C0         Fit:         General         Help           jmber of         Processors         2Core(s) / 2Thread(s)         F2t:         Previous         Values           icrocode         Revision         1c         F3t:         Optimized         Defaults           t         Info         Not         Applicable         F4t:         Save & Exit   | Butesson 10     Document     Fit General Help       Lepping     Cone     Fit General Help       Imber of Processors     2Core(s) / 2Thread(s)     Fit Previous Values       Ecrocode Revision     1c     Fit Save & Exit       T Info     Not Applicable     Fit Save & Exit       ESC: Exit     ESC: Exit     Fit Save & Exit   | Butesson 10     Document     Fit General Help       Lepping     Cone     Fit General Help       Imber of Processors     2Core(s) / 2Thread(s)     Fit Previous Values       Ecrocode Revision     1c     Fit Save & Exit       T Info     Not Applicable     Fit Save & Exit       ESC: Exit     ESC: Exit     Fit Save & Exit  | Buttlesson 10     Doublesson 10     Fit General Help       Lepping     Co     Fit General Help       Imber of Processors     2Core(s) / 2Thread(s)     Fit Previous Values       Lorocode Revision     1c     Fit General Help       T Info     Not Applicable     Fit Save 8 Exit       ESC: Exit     ESC: Exit     Esc: Exit   
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   |

- The following point is only for case of BIOS reset
- OMLEX MX comes with complete and correct BIOS settings
- To enter BIOS on OMLEX MX press Del or "Delete" key on the keyboard

## Step 2 — Checking BIOS



- With right arrow key select Advanced
- With *down arrow key* select **CPU Configuration** and press *enter*
- Then go on botom of page with *down arrow key*
- Select Intel Virtualization Technology and if is Disabled change to Enabled with Space key

A Press *Esc* to return to the main menu

#### Step 3 — Save nad Exit

Aptio Setup Utility - Copyr Main Advanced Chipset Boot Security Save & B	American Megatrends, inc.
Save Changes and Reset Discard Changes and Reset Restore Defaults Save as User Defaults Restore User Defaults	Reset the system after saving the changes.
	++: Select Screen 14: Select Item Enter: Select +/-: Change Opt. F1: General Help F2: Previous Values F3: Optimized Defaults F4: Save & Exit ESC: Exit
Version 2.15.1236. Copyright (C) 20	012 American Megatrends, Inc.

- With arrow keys select Save & Exit
- Select Save Changes and Reset and press two times *enter*
- (i) Computer will now restart

## Step 4 — Screen configuration

Display	,		•
Configure screen settings and layout	Virtual1		[•
	Resolution:	1440×900	
Virtual1	Refresh rate:	59.9 Hz	[•
	Rotation:	None	[•
	Reflection:	None	[•
Configure new displays when connected	Identify D	splays 🚽	Apply
@ Help			X Close

- To configure screen, please connect all displays to the OMLEX MX terminal
- Open window by pressing
  Ctrl+Win+D (WIN means Windows
  Key, or Super Key)

#### Step 5 — Network configuration

MX Configuration     Main Menu     Choice     Network Configuration   Display Configuration     Exit     OK     MX Configuration     Main Menu     Main Menu	Main Menu Choice Network Configuration Display Configuration Displ	Main Menu Choice Network Configuration Display Configuration Display Configuration Display Configuration		
Choice       Value         Network Configuration       MX Terminal IP Address       142.254.20.10         Display Configuration       MX Network Mask       255.255.0.0         OT IP Address       142.254.20.1	Choice     Value       Network Configuration     MX Terminal IP Address       Display Configuration     255.255.0.0       OT IP Address     142.254.20.1	Choice          Network Configuration         Display Configuration	MX Configuration + • ×	MX Configuration + • ×
Choice       Value         Network Configuration       MX Terminal IP Address         Display Configuration       255.255.0.0         OT IP Address       142.254.20.1	Choice       Value         Network Configuration       MX Terminal IP Address         Display Configuration       255.255.0.0         OT IP Address       142.254.20.1	Choice       Value         Network Configuration       MX Terminal IP Address         Display Configuration       MX Network Mask         QT IP Address       142.254.20.1	Main Menu	Network Configuration
Display Configuration MX Network Mask 255.255.0.0 142.254.20.1	Display Configuration MX Network Mask 255.255.0.0 142.254.20.1	Display Configuration MX Network Mask 255.255.0.0 142.254.20.1	Choice	
Exit OK Main Menu OK	Exit OK Main Menu OK	Exit OK Main Menu OK		MX Network Mask 255.255.0.0
			Exit OK	Main Menu OK

To open **MX Configuration** move your mouse to the most left monitor to the left bottom corner and click on icon

- (i) Icon is hidden wait few seconds for it to appear
- Open Network Configuration by clicking on it
- By clicking on Values you can edit them
- 闭 jak se vrátit do menu? ukládání?

# Step 6 — Display Configuration

MX Configuration + • ×	
Main Menu Choice Network Configuration Display Configuration Exit OK	MX Configuration       Display Configuration       Choice     Value       Pesolution     1280x960       Matrix     2x1       Main Menu     OK

- Open **Display Configuration** by clicking on it
- By clicking on Values you can edit them
- 闭 jak se vrátit do menu? ukládání?

## Step 7 — Mouse Configuration

80	Settings		+ • ×	Æ	Mouse and Touchpad	• •
Settings Customize your de	esktop	I	٩	6	Mouse and Touchpad Configure pointer device behavior and appearance	r 4
Personal				D	evices Behavior Theme	
Appearance	Desktop	File Manager		ε	Device: VMware VMware Virtual USB Mouse	
Can		Preferred			Enable this device	
Notifications	Panel	Applications			Buttons and Feedback Buttons	1
Window Manager	Window Manager Tweaks	Workspaces			Right-handed O Left-handed	
Hardware					Reverse scroll direction	
-		K Mouse and			Pointer Speed Acceleration:	2.0
Display	Keyboard	Mouse and Touchpad			Sensitivity:	4 px
Power Manager	Removable Drives and Media				Reset to Defaults	
System	E terre					
e Help	<sup>^</sup>	All Settings	% <u>C</u> lose		😥 Help 🌾 All Set	tings 🛛 💥 <u>C</u> lose

✓ Open window by pressing Ctrl+Win+S (WIN means Windows Key, or Super Key)

## • Slect Mouse and Touchpad

(i) Now you can edit your mouse as you please